

SID II address locations

Sound Interface Device (SID) Registers are located in the area \$d400 - \$d7ff. The SID chip has been provided with A0 to A4 for 32 different registers. The result is that every 32-byte area in this 1K block is a mirror of every other.

Address/ bit	\$d4xx-\$d7xx										reg. \$00 - \$1f					
	A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0
\$d400	1	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0
\$d420	1	1	0	1	0	1	0	0	0	0	1	0	0	0	0	0
\$d440	1	1	0	1	0	1	0	0	0	1	0	0	0	0	0	0
\$d460	1	1	0	1	0	1	0	0	0	1	1	0	0	0	0	0
\$d480	1	1	0	1	0	1	0	0	1	0	0	0	0	0	0	0
\$d4a0	1	1	0	1	0	1	0	0	1	0	1	0	0	0	0	0
\$d4c0	1	1	0	1	0	1	0	0	1	1	0	0	0	0	0	0
\$d4e0	1	1	0	1	0	1	0	0	1	1	1	0	0	0	0	0
\$d500	1	1	0	1	0	1	0	1	0	0	0	0	0	0	0	0

Table 1

Connecting the auxiliary logic (IO2) to A5 the area is divided according to Table 1. Both SID chips are mirrored in the whole address space, but we recommend to use \$d400-\$d41f for SID0 and \$d420-\$d43f for SID1.

